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Applicant,Title of invention,Abstract — e.g. computer semiconductor

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mold piezoelectric AND

AND

ceramic substrate AND

AND

AND

AND

Date of publication of application — e.g.19980401 - 19980405

-

AND

IPC — e.g. D01B7/04 A01C11/02

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Applicant,Title of invention,Abstract — e.g. computer semiconductor

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mold piezoelectric AND

AND

dirt dust particle particulate OR

AND

AND

AND

Date of publication of application — e.g.19980401 - 19980405

-

AND

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	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	96	(MOLD$3 and piezoelectric).ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/19 13:41	
2	BRS	L2	1844	Piezoelectric$ same (array or matrix) AND STOP$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/19 15:16	
3	BRS	L3	1074	Piezoelectric$ same (ELEMENT OR MEMBER) same stop	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/19 15:17	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
4	BRS	L4	1035	13 not 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/19 15:42	
5	BRS	L5	16	(dirt or dust or particle or particulate) adj2 free same piezoelectric	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/19 15:43	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	IS&R	L1	1318	(310/322,334).CCLS.	USPAT	2005/09/19 10:06	
2	IS&R	L2	266	(310/322,334).CCLS.	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2005/09/19 10:11	
3	IS&R	L3	437	(310/328).CCLS.	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2005/09/19 10:23	
4	IS&R	L4	1589	(310/328).CCLS.	USPAT	2005/09/19 10:23	